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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	TOT CLAIMS	IND CLAIMS
10/595,742	05/08/2006	2827	900	JP920030200US1	6	2

CONFIRMATION NO. 4737

24241  
IBM MICROELECTRONICS  
INTELLECTUAL PROPERTY LAW  
1000 RIVER STREET  
972 E  
ESSEX JUNCTION, VT 05452

FILING RECEIPT



Date Mailed: 08/18/2008

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

**Applicant(s)**

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**Power of Attorney:** The patent practitioners associated with Customer Number 24241

**Domestic Priority data as claimed by applicant**

This application is a 371 of PCT/JP04/16296 11/04/2004

**Foreign Applications**

JAPAN 2003-377485 11/06/2003

**If Required, Foreign Filing License Granted:** 08/14/2008

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/595,742**

**Projected Publication Date:** 11/27/2008

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Semiconductor Storage Device and Burst Operation Method

**Preliminary Class**

365

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